(19) World Intellectual Property Organization

International Bureau



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(43) International Publication Date 5 August 2004 (05.08.2004)

PCT

(10) International Publication Number WO 2004/066355 A3

(51) International Patent Classification⁷: H04L 9/00, 9/32

G06F 11/30.

(21) International Application Number:

PCT/US2004/001581

(22) International Filing Date: 20 January 2004 (20.01.2004)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

60/440,734

17 January 2003 (17.01.2003) US

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- (74) Agents: TRIPOLI, Joseph et al.; c/o Thomson Licensing, Inc., Two Independence Way, Suite 200, Princeton, New Jersey 08540 (US).
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

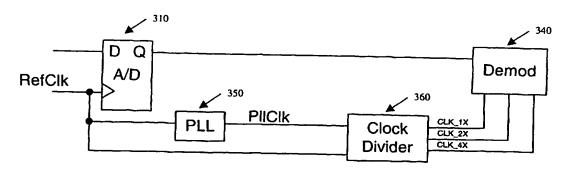
(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments
- (88) Date of publication of the international search report: 11 November 2004

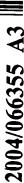
For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: A METHOD FOR USING A SYNCHRONOUS SAMPLING DESIGN IN A FIXED-RATE SAMPLING MODE



(57) Abstract: The present application generally relates to apparatuses such as television signal processing apparatus that process radio frequency signals. More specifically, the present application is particularly useful in integrated circuits that must combine circuitry operating in a synchronous-sampling mode that must be adapted for use with a fixed rate sampling mode application. According to an exemplary embodiment, the television signal processing apparatus 100 comprises a source of a fixed rate digital signal 220, signal processing circuitry 240 operating in a synchronous-sampling mode wherein the signal processing circuitry 240 comprises a signal representing a symbol rate, and an interpolator 230 for processing the fixed rate digital signal to yield samples at the symbol rate.





INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/01581

A. CLASSIFICATION OF SUBJECT MATTER	<u>l</u>	FC170304/01581	
IPC(7) : G06F 11/30; H04L 9/00, 9/32			
US CL : 713/200			
According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols)			
U.Ş. : 713/200			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
and the fields searched			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)			
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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category * Citation of document, with indication, where	appropriate, of the releva	int passages	Relevant to claim No.
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Y US 3,943,346 (Urkowitz et al) 9 March 1976 (00	US 3,943,346 (Urkowitz et al) 9 March 1976 (09.03.1976), column 8 line 53-67; column 2		
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Date of the actual completion of the international search	the international search Date of mailing of the international search report		
30 August 2004 (30.08.2004)	12 05	P 2004	report
Name and mailing address of the ISA/US		.P 4004	
Mail Stop PCT, Atm: ISA/US			
Commissioner for Patents	Ayaz R Sheikh		
P.O. Box 1450 Alexandria, Virginia 22313-1450 Telephone No. 363-305-0900			
Facsimile No. (703) 305-3230			
orm PCT/ISA/210 (second sheet) (January 2004)			